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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR		ATTORNEY DOCKET NO.	
09/620,649	07/20/00	KAYA		C	TI-23686.I
MM91/0629		, 7	EXAMINER		
JACQUELINE J GARNER ESQ TEXAS INSTRUMENTS INCORPORATED P O BOX 655474 M S 219				GOODL	VIN.D
PALLAS TX 7	5265	19		2822	
				DATE MAILED	•
					06/29/01

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

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	• •	Application No.	Applicant(s)					
Office Action Summary		09/620,649	KAYA, CETIN NMI					
		Examiner	Art Unit					
		David Goodwin	2822					
Period fo	The MAILING DATE of this communication appe or Reply	ears on the cover sheet with the co	rrespondence address					
THE I - Externafter - If the - If NO - Failu - Any r	ORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. Insions of time may be available under the provisions of 37 CFR 1.15 SIX (6) MONTHS from the mailing date of this communication. Period for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period were to reply within the set or extended period for reply will, by statute, eply received by the Office later than three months after the mailing and patent term adjustment. See 37 CFR 1.704(b).	36 (a). In no event, however, may a reply be ting within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication, D (35 U.S.C. § 133).					
1)⊠	Responsive to communication(s) filed on 18 h	May 2001 .						
2a)⊠	This action is FINAL. 2b) ☐ Th	is action is non-final.						
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Dispositi	ion of Claims							
4)⊠	Claim(s) 12-20 is/are pending in the application	on.						
	4a) Of the above claim(s) is/are withdraw	wn from consideration.						
5)⊠ Claim(s) <u>20</u> is/are allowed.								
6)⊠)⊠ Claim(s) <u>12-19</u> is/are rejected.							
7)								
8) 🗌	8) Claims are subject to restriction and/or election requirement.							
Applicati	ion Papers							
9) 🗆	The specification is objected to by the Examine	er.						
10) The drawing(s) filed on is/are objected to by the Examiner.								
11) The proposed drawing correction filed on is: a) approved b) disapproved.								
12)	12) The oath or declaration is objected to by the Examiner.							
Priority (ınder 35 U.S.C. \$ 119							
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. \$ 119(a)-(d) or (f).								
	☐ All b)☐ Some * c)☐ None of:							
1. Certified copies of the priority documents have been received.								
	2. Certified copies of the priority documents have been received in Application No							
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).								
* See the attached detailed Office action for a list of the certified copies not received. 14) Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).								
14)	Acknowledgement is made of a claim for dome	esuc priority under 35 U.S.C. § 11	ਰ(ਫ) .					
Attachmen	t(s)							
15) Not 16) Not	ice of References Cited (PTO-892) ice of Draftsperson's Patent Drawing Review (PTO-948) rmation Disclosure Statement(s) (PTO-1449) Paper No(s)	19) Notice of Informal	ry (PTO-413) Paper No(s) Patent Application (PTO-152)					

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DETAILED ACTION

This office action is in response to the amendment filed on May 18, 2001.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

Claims 12, 13, 15, and 16 are rejected under 35 U.S.C. 102(e) as being anticipated by Van Buskirk (US patent 6,001,689).

Van Buskirk teaches an integrated circuit comprising a floating gate memory array. Wherein the array comprises a plurality of gate stacks having a channel dielectric (61), a polysilicon floating gate (51), a floating gate dielectric (71), and a polysilicon gate electrode (41) (column 4 lines 1-10). The structure further comprises trenches and moats (fig 7A) formed between the stacks (column 5 lines 35-55). First oxide spacers (121) and oxide layer (101) are formed between the stacks and subsequently planarized to expose the polysilicon gate electrode (41) (column 5 lines 35-65).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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Claims 18 and 19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Van Buskirk (US patent 6,001,689) in view of Woo (US patent 5,926,711).

Van Buskirk teaches all aspects of the claimed invention above. However, Van Buskirk does not teach the use of hemispherical grained silicon on the floating gate.

Woo teaches a floating gate transistor wherein the floating gate (24C) is formed of amorphous silicon which is converted to hemispherical grains of silicon (fig 3f, column 4 lines 35-55).

It would have been obvious to one of ordinary skill in the art to use a floating gate having hemispherical grains of silicon in order to improve the capacitive coupling of the floating and control gates.

Claims 14 and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Van Buskirk (US patent 6,001,689) in view of Chan (US patent 6,051,467).

Van Buskirk teaches all aspects of the claimed invention above. However, Van Buskirk does not teach the thickness of the oxide layer in the ONO intergate dielectric.

Chan teaches that a typical thickness for the oxide layer in an ONO intergate dielectric is between 50 and 100 angstroms (column 3 lines 40-50).

It would have been obvious to one of ordinary skill in the art to use an oxide with a thickness of between 50 and 100 angstroms in an ONO intergate dielectric order to provide sufficient gate separation and capacitative coupling.

Further it would have been an obvious matter of design choice bounded by well known manufacturing constraints and ascertainable by routine experimentation and optimization to choose the particular claimed dielectric isolation dielectric thickness

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limitations because applicant has not disclosed that the limitations are for a particular unobvious purpose, produce an unexpected result, or are otherwise critical, and it appears prima facie that the process would possess utility using another isolation dielectric thickness. Indeed, it has been held that optimization of range limitations are prima facie obvious absent a disclosure that the limitations are for a particular unobvious purpose, produce an unexpected result, or are otherwise critical.

Allowable Subject Matter

Claim 20 is allowed.

The following is a statement of reasons for the indication of allowable subject matter: The prior art does not teach or suggest a semiconductor device comprising a plurality of floating gate stacks with dielectric isolation regions, a peripheral dielectric disposed outwardly from the floating gate stacks and a peripheral region of the substrate, and at least one peripheral gate disposed outwardly from the peripheral region of the substrate.

Response to Arguments

Applicant's arguments filed on May 18, 2001 have been fully considered but they are not persuasive.

The applicant claims that the application receives benefit from a provisional application filed on September 30, 1997.

However the parent application (09/168,047) of this application was filed on October 7, 1998. October 10, 1998 is more then 12 months after the filing date of the

provisional application and is therefore unable to claim priority from said provisional application.

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Goodwin whose telephone number is (703)308-4931. The examiner can normally be reached on Mon-Fri from 9:00am to 4:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl Whitehead can be reached on (703)308-4940. The fax phone numbers for the organization where this application or proceeding is assigned are (703)308-7722 for regular communications and (703)308-7722 for After Final communications.

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Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703)308-2774.

DJG

June 27, 2001

Mary Wilczewski Primary Examiner Page 6